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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>		FILING DATE: January 10, 2000	
		APPLICANT: Seamus Paul Whiston	
Sheet	1	of	1
		GROUP ART UNIT: 2813	EXAMINER: Y.B. Huynh

## U.S. PATENT DOCUMENTS

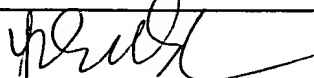
Examiner's Initials#	Cite No.	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication or of issue of Cited Document MM-DD-YY
		Number	Kind Code		

## FOREIGN PATENT DOCUMENTS

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## OTHER ART — NON PATENT LITERATURE DOCUMENTS

Examiner's Initials#	Cite No	Include name of the author (in CAPITAL LETTERS) title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, relevant page(s), volume-issue number(s), publisher, city and/or country where published.	Translation (Y/N)	
yk		Contiero, C. et al, "LDMOS implementation by large tilt implant in 0.6µm BCD5 process, flash memory compatible", IEEE, 1996, pp. 75-78		

EXAMINER		DATE CONSIDERED	10/8/02
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#EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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